DINOMO: An Elastic, Scalable, High-Performance Key-Value Store for Disaggregated Persistent Memory

Sekwon Lee^{*}, Soujanya Ponnapalli, Sharad Singhal, Marcos K. Aguilera, Kimberly Keeton, Vijay Chidambaram









*On the job market

- Persistent Memory (PM)
 - Non-volatile like storage and byte-addressable like DRAM
 - High performance close to DRAM
 - Cost per GB >>>> HDD or SSD



PCM





Intel Optane DC PM



CXL-SSD

- Persistent Memory (PM)
 - Non-volatile like storage and byte-addressable like DRAM
 - High performance close to DRAM
 - Cost per GB >>>> HDD or SSD
 - Ensuring high utilization is more critical for cost efficiency



PCM



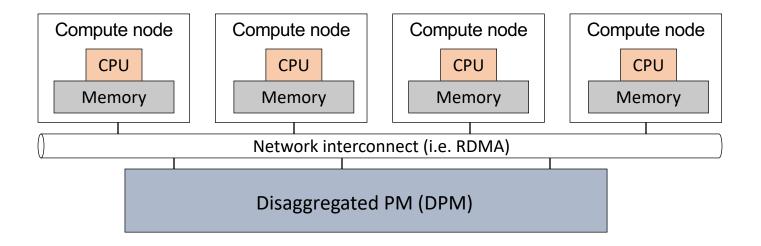


Intel Optane DC PM

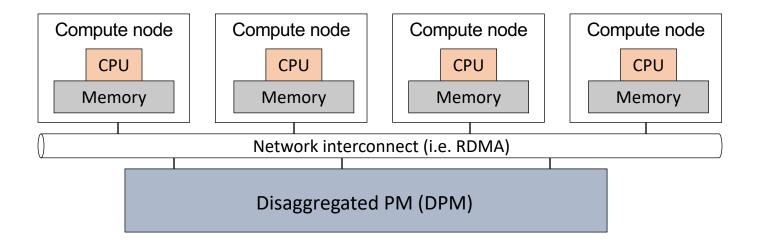


CXL-SSD

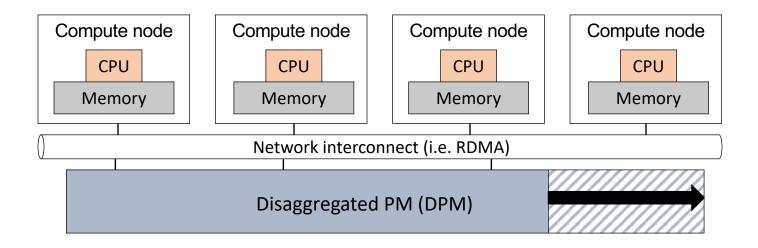
- Disaggregated Persistent Memory (DPM)
 - + Share PM → Increase utilization, Reduce TCO (Total Cost Ownership)



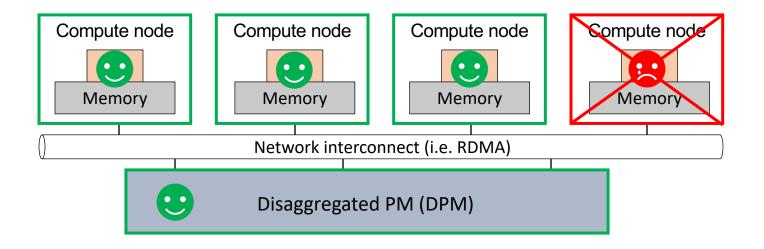
- + Share PM → Increase utilization, Reduce TCO (Total Cost Ownership)
- + Disaggregate PM \rightarrow Scale resources independently, Separate failure domains



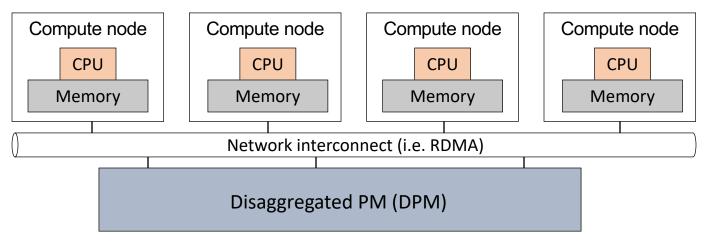
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- Access PM over network → Network latency >> PM latency



Data processing system done right for DPM

To benefit from the independence of scaling resources and failure, it must be elastic and scalable

Despite expensive networking overheads, it must provide high performance

Network interconnect (i.e. RDMA)

Disaggregated PM (DPM)

Key-Value Store (KVS) for DPM

• Simple key-value APIs: get, put, update, delete ...

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- Support various applications under cloud environment
 - Require dynamic working sets/sizes and non-uniform workload patterns with varying skew

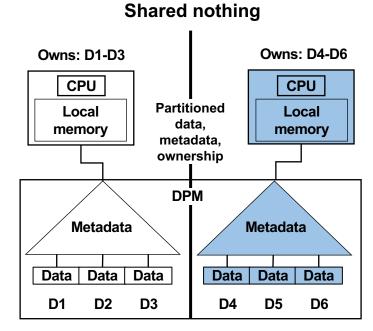
Key-Value Store (KVS) for DPM

- Simple key-value APIs: get, put, update, delete ...
- Support various applications under cloud environment
 - Require dynamic working sets/sizes and non-uniform workload patterns with varying skew
- Goals of ideal DPM KVS
 - High common-case performance
 - Scalability with the amount of provisioned resource
 - Fast reconfiguration to change the amount of resource elastically

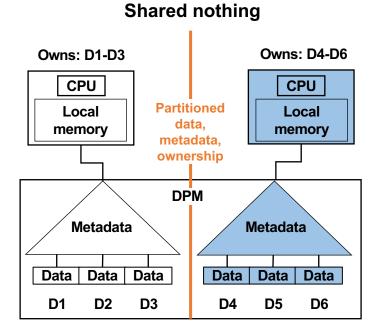
• Architectural limitations in achieving all three goals

KVSs Goals	AsymNVM	Clover
High performance	\checkmark	×
Scalability	\checkmark	x
Lightweight reconfiguration	X	\checkmark

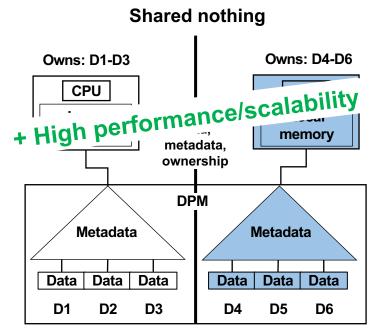
- AsymNVM¹
 - Exclusive ownership to partitioned data/metadata



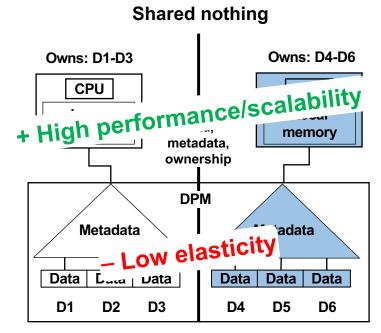
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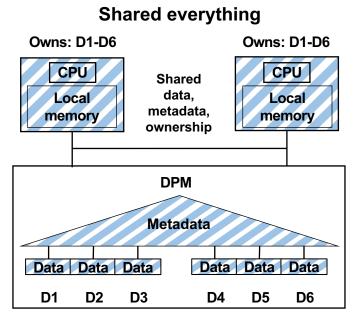
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 - + Cache data without consistency overheads
 - + Preserve data locality of caches



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 - Expensive data reorganization upon reconfigurations

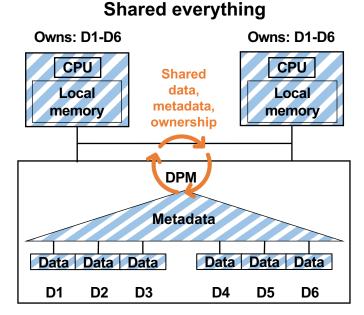


- Clover¹
 - Share data, metadata, and ownership



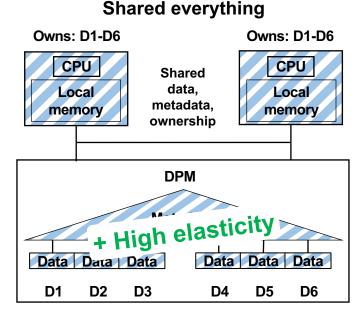
* Hatched and mixed color: shared ownership

- Clover¹
 - Share data, metadata, and ownership



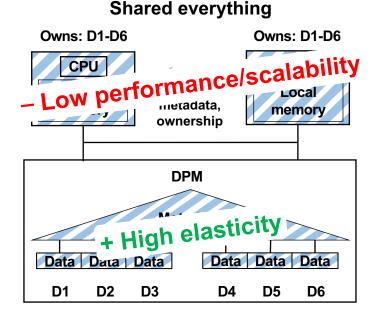
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- Clover¹
 - Share data, metadata, and ownership
 - + No data reorganization upon reconfigurations
 - Expensive data consistency overheads between caches
 - Lack of data locality of caches



^{*} Hatched and mixed color: shared ownership

• Architectural limitations in achieving all three goals

KVSs Goals	Clover	AsymNVM
High performance	X	\checkmark
Scalability	×	\checkmark
Lightweight reconfiguration	\checkmark	x



First DPM KVS achieving high performance, scalability, and fast reconfiguration simultaneously

KVSs Goals	DINOMO	AsymNVM	Clover
High performance	\checkmark	\checkmark	X
Scalability	\checkmark	\checkmark	X
Lightweight reconfiguration	\checkmark	X	\checkmark

https://github.com/utsaslab/dinomo



First DPM KVS achieving high performance, scalability, and fast reconfiguration simultaneously

Adapt techniques (ownership partitioning, adaptive caching, etc.) from storage research community for DPM

Full end-to-end implementations including KVS control plane, data plane, and client

Upto **10x** better performance at scale and elasticity

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Outline

- Ownership partitioning
- Disaggregated adaptive caching
- Evaluation
- Discussion

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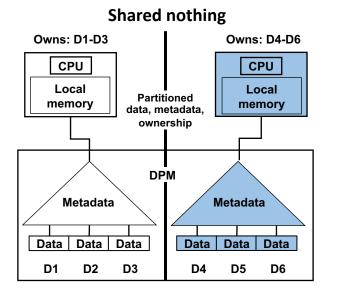
Goals and design techniques

Goal	Dinomo technique
High performance	
Scalability	
Lightweight reconfiguration (Elasticity)	

Goals and design techniques

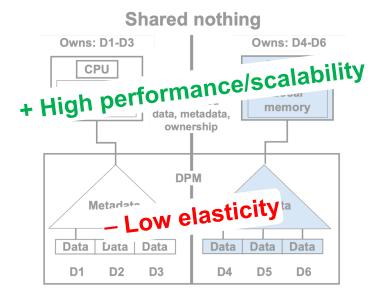
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High performance	
	Ownership partitioning
Scalability	Ownership partitioning
Lightweight reconfiguration (Elasticity)	Ownership partitioning

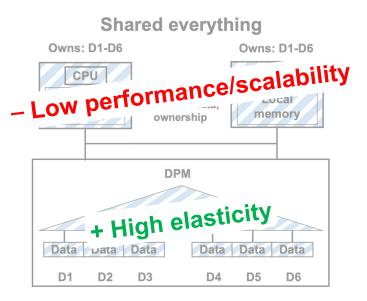
Hybrid Architecture



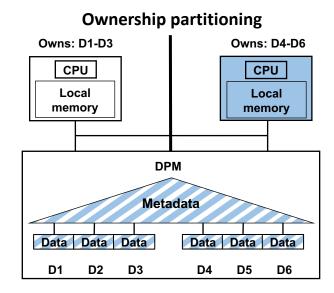
Shared everything Owns: D1-D6 Owns: D1-D6 CPU CPU Shared Local Local data, metadata, memory memory ownership DPM Metadata Data Data Data Data Data Data D1 D2 D3 D4 D5 D6

Hybrid Architecture





Hybrid Architecture

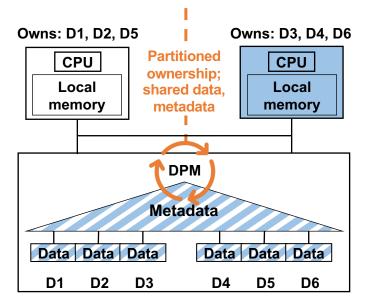


Insight: Data access and ownership can be an independent consideration owing to disaggregation

Approach: Partition ownership across compute nodes while sharing access to data through DPM

Ownership Partitioning

- Shared data/metadata
- Partitioned ownership



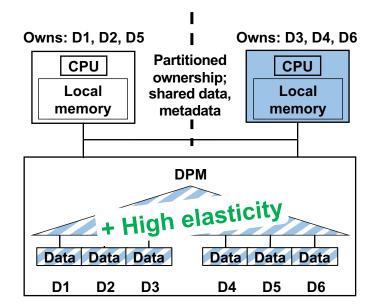
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Ownership Partitioning

Shared data/metadata

+ Allow fast reconfiguration without expensive data reorganization

Partitioned ownership



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Ownership Partitioning

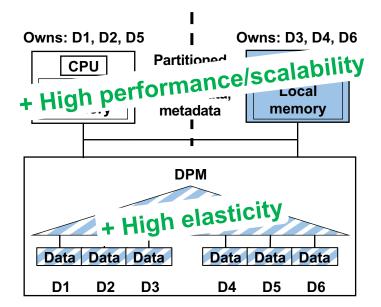
Shared data/metadata

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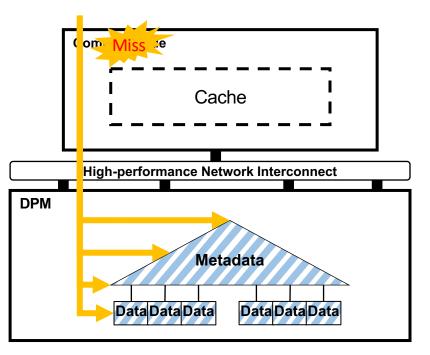
Goals and design techniques

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High performance	Disaggregated adaptive cache
	Ownership partitioning
Scalability	Ownership partitioning
Lightweight reconfiguration (Elasticity)	Ownership partitioning

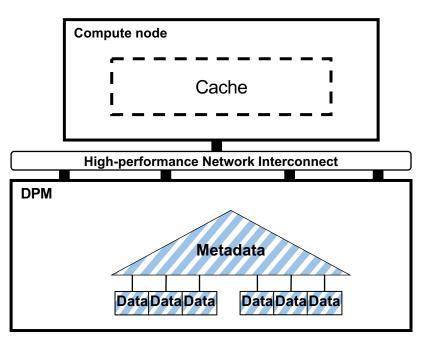
Number of network round trips significantly impacts on overall system performance

- Number of network round trips significantly impacts on overall system performance
- Cache data or metadata into the memory of compute nodes to reduce round trips to DPM
 - Important to minimize cache misses

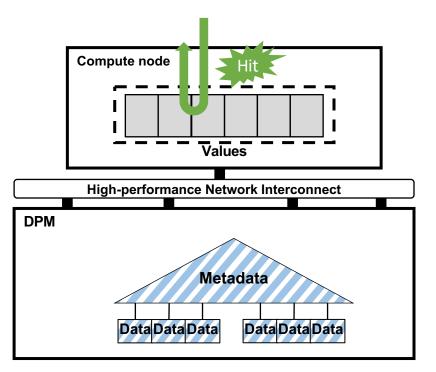
- Cache miss \rightarrow multiple RTs
 - Traverse metadata index structures in DPM
 - Fetch data from DPM



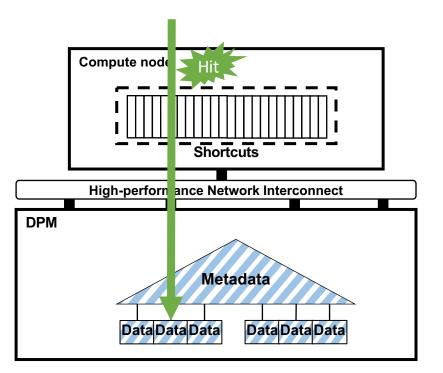
- Static policy
 - Value
 - Shortcut



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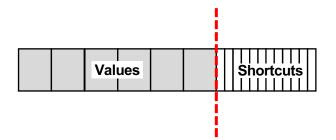




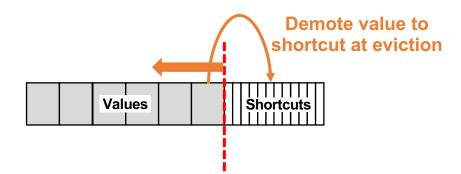
Answer: Efficient ratio depends on workload patterns and available memory space

We need an adaptive policy changing ratio between values and shortcuts!

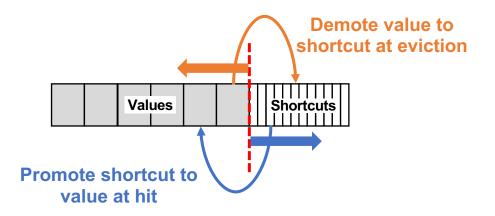
- Adaptive policy
 - Change the boundary via demotion and promotion



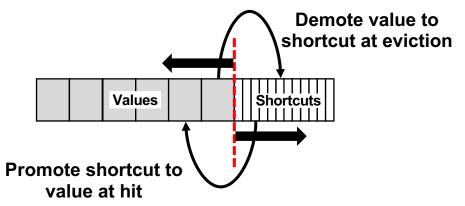
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- Adaptive policy
 - Change the boundary via demotion and promotion
 - Promotion policy considering sizes, hit costs, and miss costs



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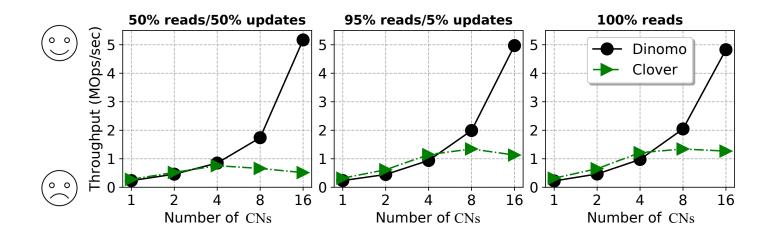
Evaluation

- How does DINOMO fare against the state-of-the-art in terms of performance and scalability?
- How elastic and responsive is DINOMO while handling changes in workloads?

Evaluation setup

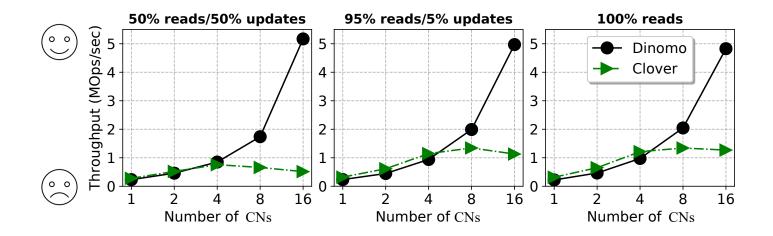
- System configuration
 - DPM: 4 threads, 110GB of DRAM to emulate PM
 - 16 CNs: 8 threads, 1GB of DRAM for caching (≈1% of the DPM)
 - Connected via 56Gbps ConnectX-3 RNICs
- Baseline
 - Performance/scalability: Clover (shared everything, shortcut-only cache)
 - Elasticity: DINOMO-N (DAC, but partition data/metadata)
- Workload
 - YCSB workloads with 8B keys and 1KB values

Performance and Scalability



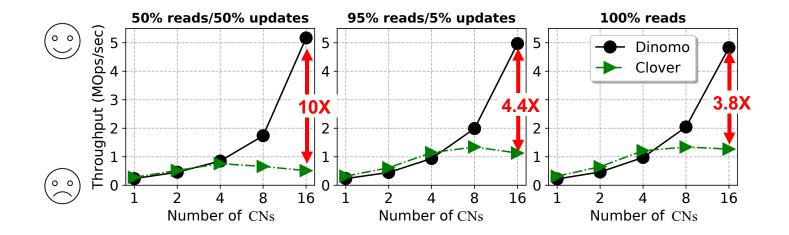
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DINOMO scales to 16 CNs, but Clover does not beyond 4 CNs

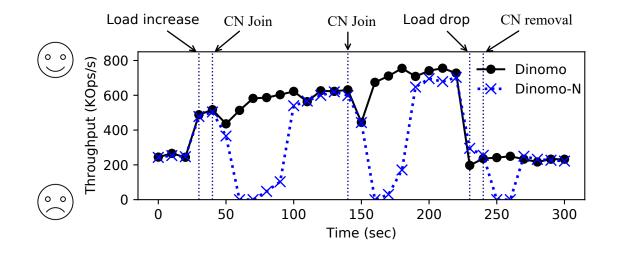


Performance and Scalability

- DINOMO scales to 16 CNs, but Clover does not beyond 4 CNs
- With 16 CNs, DINOMO outperforms Clover upto 10x

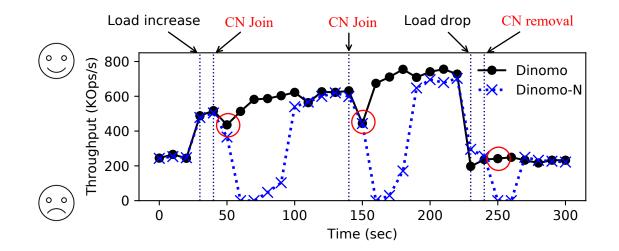


Elasticity



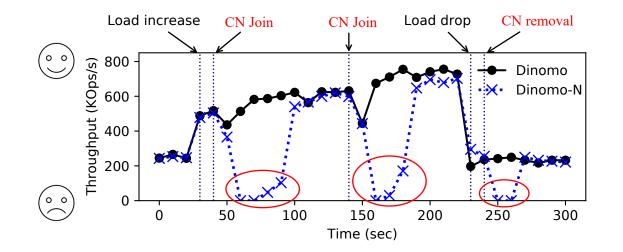
Elasticity

• DINOMO: Brief throughput dips when adding/removing CNs



Elasticity

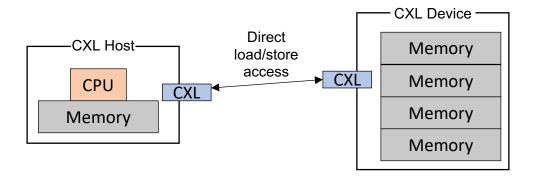
- DINOMO: Brief throughput dips when adding/removing CNs
- DINOMO-N: Throughput dips for 20-40 seconds due to expensive data reorganization



Outline

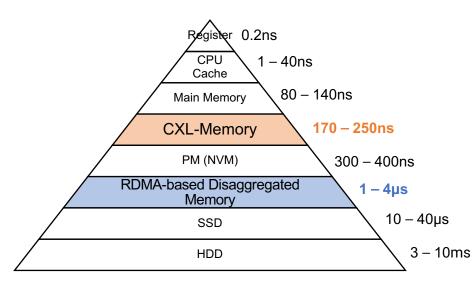
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- CXL (Compute Express Link)
 - A cache-coherent interconnect over PCIe
 - CXL.memory for memory expansion (Type 3)



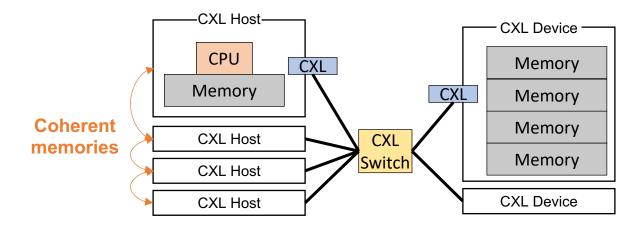
• CXL VS. RDMA-based disaggregation

- Lower access latency
 - CXL (170 250ns), RDMA (1 4µs)



• CXL VS. RDMA-based disaggregation

- Lower access latency
 - CXL (170 250ns), RDMA (1 4us)
- Hardware-guaranteed coherence



• Ownership partitioning in the context of CXL

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 - Avoid cache coherence overheads
- Ownership partitioning → Avoid coherence traffic between CXL-enabled hosts and devices

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 - A key to improve performance in RDMA-based disaggregation

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- Much lower access latency to disaggregated memory over CXL
- Unclear if caching would be still useful for low-latency medias
 - e.g., page cache bypass in PM file systems to avoid cachemanagement and data-copy overheads

- Caching data to the local memory of compute nodes has been a key to improve performance in disaggregation settings
- With CXL, access latency to disaggregated memory becomes much lower than RDMA
- Unclear if caching would be still useful for low-latency medias
 - e.g., bypassing page cache in PM file systems to avoid cachemanagement and data-copy overheads
 - Future work: How to utilize host local memory well



- First KVS for DPM achieving high performance, scalability, and elasticity simultaneously
- Use a novel combination of techniques, ownership partitioning and disaggregated adaptive cache
- Experimentally show DINOMO can scale performance and efficiently react to reconfigurations
- Try our KVS: <u>https://github.com/utsaslab/dinomo</u>